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Processor having multiple program counters and trace buffers outside an execution pipeline

Publication number: TW406239 (B)

Publication date: 2000-09-21

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Classification:

- **international:** *G06F9/38; G06F9/46; G06F9/38; G06F9/46; (IPC1-7): G06F9/30; G06F9/38; G06F9/40*

- **European:** *G06F9/38D4; G06F9/38E2; G06F9/38E4; G06F9/38H2*

Application number: TW19980120951 19981216

Priority number(s): US19970992375 19971216

Also published as:

	WO9931580 (A1)
	US6182210 (B1)
	US2001014941 (A1)
	US6493820 (B2)
	US6247121 (B1)

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Abstract of TW 406239 (B)

In one embodiment of the invention, a processor includes an execution pipeline to concurrently execute at least portions of threads, wherein at least one of the threads is dependent on at least another one of the threads. The processor also includes detection circuitry to detect speculation errors in the execution of the threads. In another embodiment, the processor includes thread management logic to control dynamic creation of threads from a program.

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